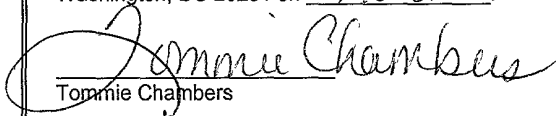# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers

Docket No: TI-30883

Serial No: TBD

Examiner: TBD

Filed: 07/10/01

Art Unit: TBD

For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION

Assistant Commissioner for Patents

Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-identified application, please amend the specification by inserting before the first line the sentence:

IN THE SPECIFICATION

--This application claims priority under 35 USC § 119(e)(1) of provisional application Serial No. 60/217,046, filed 07/10/00.--
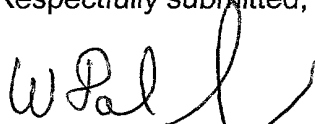
REMARKS

Claims 1-10 remain in the application for consideration by the Examiner.

An early and favorable action is respectfully requested.

Should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,

W. Daniel Swayze, Jr.
Attorney for Applicant
Reg. No. 34,478

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